

ABSTRACT OF THE DISCLOSURE

A charge transfer device according to the present invention is one having a charge transfer portion in which a plurality of electrode pairs are formed above a transfer channel, and includes means for commonly wiring the plurality of electrode pairs forming N ($N=2, 3, 4, \dots$ natural numbers) bits of the charge transfer portion bits so that electrode pairs of each half bit can be independently driven at every N bits, means for, in a normal operation, inputting the electrode pairs of each half bit with the same drive pulse to operate it by a two-phase complementary drive, and means for, in an N -time speed operation, inputting the electrode pairs of N bits with N pairs of complementary drive pulses to operate them by a $2N$ -phase complementary drive.